		STUDY MODULE D	ESCRIPTION FORM			
	of the module/subject			Code		
Communication computer interfaces			Profile of study	1010324381010326896 Year /Semester		
Field of study			(general academic, practical)			
Electrical Engineering			general academic	4/8		
Elective path/specialty Microprocessor Control Systems in			Subject offered in: Polish	Course (compulsory, elective) obligatory		
Cycle of study:			Form of study (full-time,part-time)			
	First-cy	cle studies	part-time			
No. of I	hours		I	No. of credits		
Lectu	re: 9 Classe	s: - Laboratory: 9	Project/seminars:	- 2		
Status	of the course in the study	program (Basic, major, other)	(university-wide, from another f			
		other	unive	ersity-wide		
Educat	ion areas and fields of sc	ience and art		ECTS distribution (number and %)		
tech	nical sciences			2 100%		
	Technical sci	ences		2 100%		
Resp	onsible for subj	ect / lecturer:	Responsible for subject	ct / lecturer:		
dr i	nż. Michał Krystkowial	k	Michał Krystkowiak			
	ail: Michal.Krystkowial	k@put.poznan.pl	email: Michal.Krystkowiak@	@put.poznan.pl		
	061 665 2388 ctrical		tel. 061 665 2388 Electrical			
	Piotrowo 3A, 60-965 P	Poznań	ul. Piotrowo 3A, 60-965 Poznań			
Prer	equisites in term	ns of knowledge, skills an	d social competencies:			
1	Knowledge	He knows the principles of opera knows the hardware layer comm	peration and configuration of basic communication interfaces. He mmunication interfaces.			
2	Skills	He can apply his knowledge in the field of electronics and information technology to the analysis of digital interfaces at the basic level. Put the program to configure parameters in order to establish data exchange.				
3	Social	He can think and act in an entrepreneurial manner in the area of operation and configuration				
	competencies	interfaces.				
	-	jectives of the course: cation protocols and interfaces. Sk	rills acquisition and implementa	ation of selected interfaces.		
	Study outco	mes and reference to the	educational results for	a field of study		
Knov	wledge:					
	ould be able to: describ 16++, K_W17+,]]	be the principles of operation of se	lected interfaces of hardware a	nd software layers -		
2. Sho	ould be able to: wybtra	nych parameters to configure com	munication protocols - [[K_W1	6++, K_W17+]]		
	ould be able to: make on the contract of the c	optimal choices Interface communi 5+]]]	cation depending on the applic	eation needs -		
Skills	s:					
1. Will be able to: apply knowledge of computing and electronics in order to implement the selected interfeksu and data transfer protocol - [[K_U21++, K_U12+]]						
	be able to: apply the s 13+, K_U21++]]	selected configuration of computer	tools to support communicatio	n protocols and interfaces -		
Soci	al competencies	:				
He can think and act in an entrepreneurial manner in the implementation of interfaces - [[K_K02 ++]]						

Assessment methods of study outcomes

Faculty of Electrical Engineering

Lecture

- continuous evaluation for each course (rewarding activity and quality perception)

Laboratory:

- test and favoring knowledge necessary for the accomplishment of problems in the area of tasks in the laboratory,
- continuous evaluation, rewarding gain skills they met the principles and methods
- assess the knowledge and skills related to the implementation of laboratory exercises, the evaluation report made ??exercise.

Get extra points for the activity in the classroom, and in particular for:

- propose to discuss further aspects of the subject,
- the effectiveness of the application of the knowledge gained during solving the given problem,
- ability to work within a team performing a task specific practice in the laboratory.

Course description

Update 2017: selected service interfaces in hardware and software, familiar with the protocols of data transfer (eg, Internet protocols, protocols used in industrial automation), types and construction of transmission media, architecture and operation of different network structures. Sample implementations, galvanic separation.

Basic bibliography:

- 1. Włodzimierz Solnik, Zbigniew Zajda: Sieć Profibus DP w praktyce przemysłowej. Przykłady zastosowań, BTC, Warszawa 2013
- 2. Marcin Peczarski:USB dla niewtajemniczonych w przykładach na mikrokontrolery STM32, BTC, Warszawa 2013
- 3. Włodzimierz Solnik, Zbigniew Zajda: Sieć Profibus DP w praktyce przemysłowej. Przykłady zastosowań, BTC, Warszawa 2013

Additional bibliography:

1. Dokumentacje techniczne firm dotyczących orpogramowania interfejsu RS and USB

Result of average student's workload

Activity	Time (working hours)
1. Lectures, laboratories, consulting	45
2. Laboratory classes, preparation for classes, reports	35

Student's workload

Source of workload	hours	ECTS
Total workload	45	2
Contact hours	35	1
Practical activities	15	1